

PROFIBUS DP and PA Stack – Technical Data

Functionality	Combi-Master	DP/V2	DP-Slave	PA-Slave
FMS Master	X	-	-	-
FMS Slave	X	-	-	-
FDL (Layer-2-Access)	X	-	-	-
Management	X	-	-	-
DP Master	X	X	-	-
DP/V1 Master	Class 2	X	-	-
DP/V2 Master	-	X	-	-
DP Slave	-	-	X	X
DP/V1 Slave	-	X	-	X
DP/V2 Slave	-	-	-	-
Function Block Shell	-	-	-	X
Function Blocks	-	-	-	AI, AO, DI, DO, Totalizer

Fieldbus Interface	Combi-Master	DP/V2	DP-Slave	PA-Slave
Number of Channels	1	1	1	1
Controller	ASPC2	ASPC2	SPC3	SPC4-2, VPC3+C, UFC100-F1, FIND-1+
Data Transfer Rates	9,6; 19,2; 45,45; 93,75; 187,5; 500; 1.500; 3.000; 6.000; 12.000 KBit/s			31,25 kBit/s

Porting	Combi-Master	DP/V2	DP-Slave	PA-Slave
Processors	x86, Motorola CPU32, Infineon C16x	Infineon C16x	Infineon C16x	Toshiba T900L, M16C62, M32C87
OS	no OS, pSOS, VxWorks	CMX-RTX	no OS	CMX-RTX , Nucleus+, embOS

Minimum Hardware Requirements	Combi-Master	DP/V2	DP-Slave	PA-Slave
CPU-Class	16 Bit	16 Bit	16 Bit	16 Bit
CPU-Cycle	10 MHz	16 MHz	8 MHz	2 MHz
ROM	320 Kbyte (C165)	256 KByte (C165)	42 KByte (C165)	100KByte (M16C)
RAM	> 256 Kbyte	> 256 KByte	> 4 KByte	> 64 KByte